

09:28:07.04301

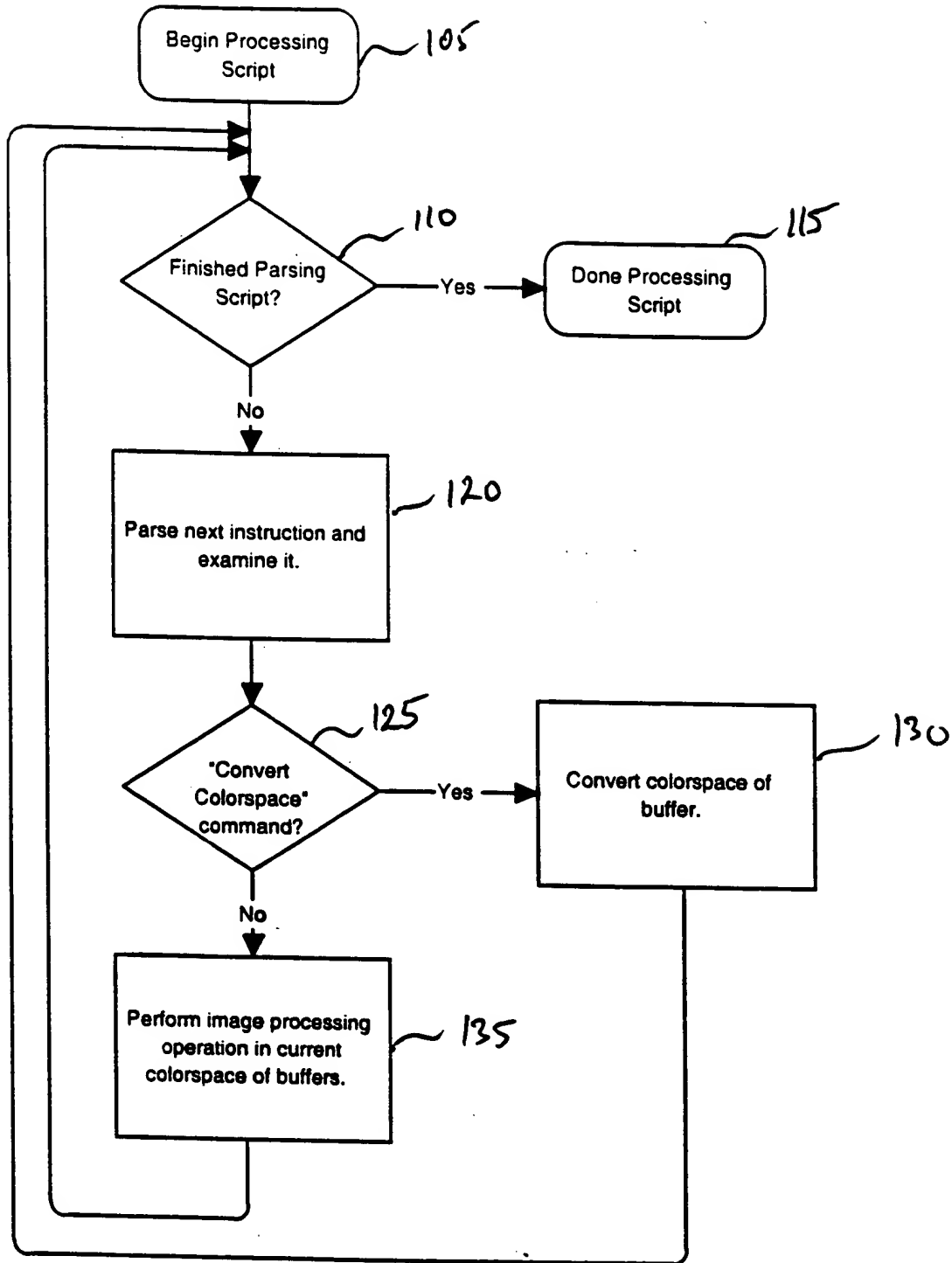


FIGURE 1

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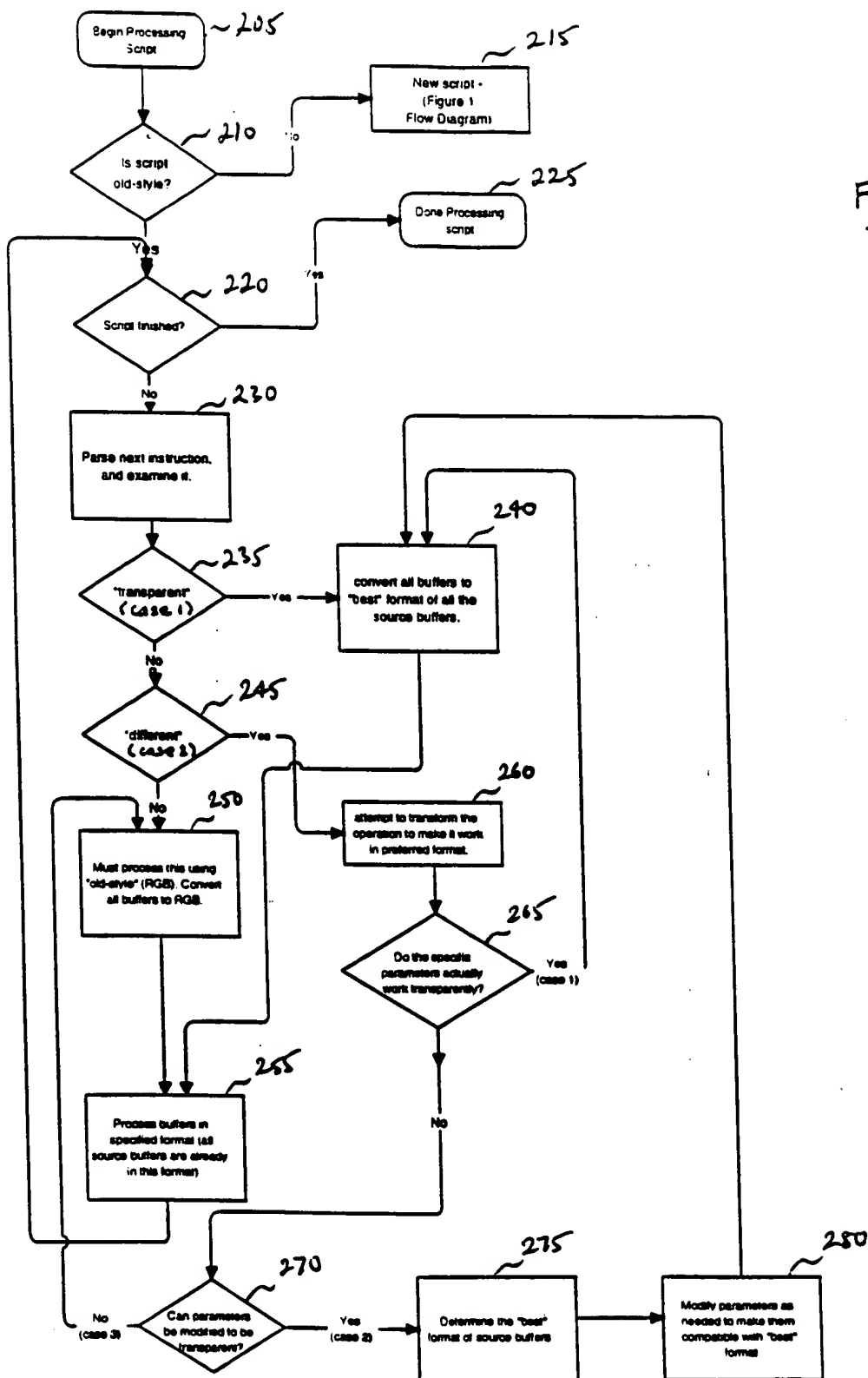


FIGURE 2

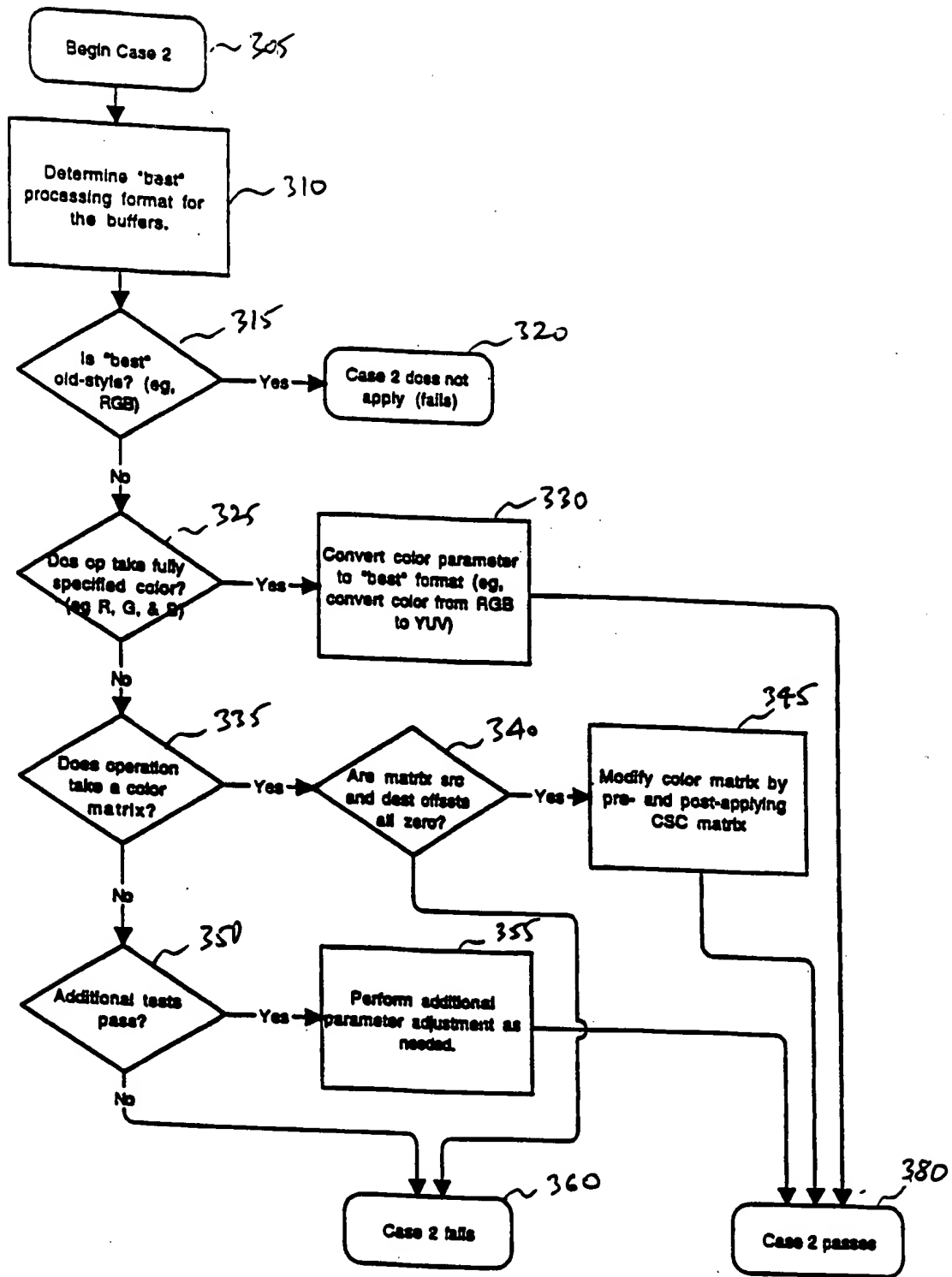


FIGURE 3

FIG. 4

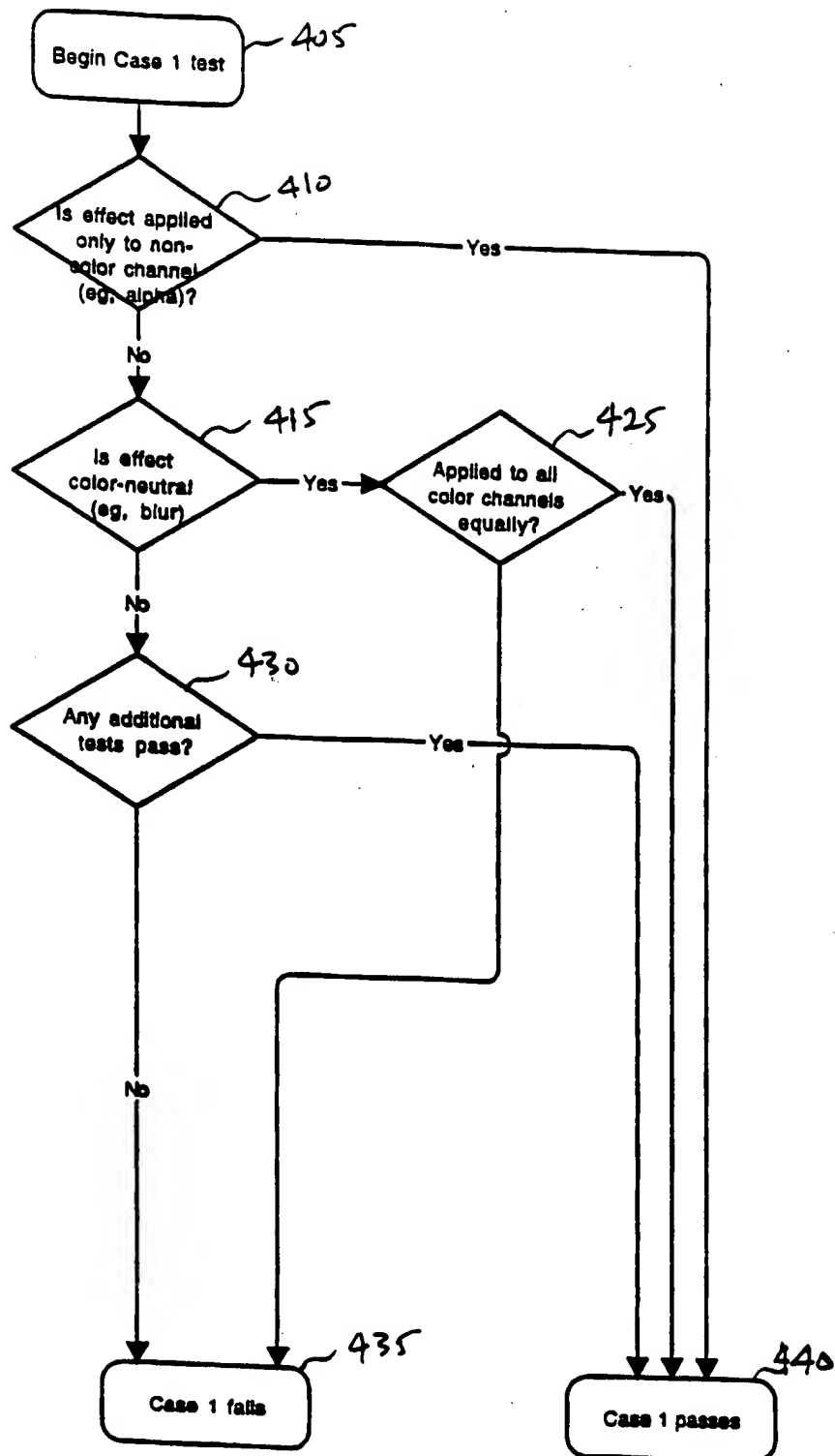


FIGURE 4

The diagram illustrates a computer system architecture centered around a vertical **BUS** (548). The components and their connections are as follows:

- Left Side Components:**
 - PROCESSOR** (542): Contains an **INSTRUCTIONS** block (562). It is connected to the bus via a bidirectional arrow.
 - MAIN MEMORY** (544): Contains an **INSTRUCTIONS** block (562). It is connected to the bus via a bidirectional arrow.
 - STATIC MEMORY** (546): Connected to the bus via a bidirectional arrow.
 - NETWORK INTERFACE DEVICE** (550): Connected to the bus via a bidirectional arrow. It is also connected to a **NETWORK** (represented by a cloud) via a lightning bolt symbol.
- Right Side Components:**
 - VIDEO DISPLAY** (549): Connected to the bus via a bidirectional arrow.
 - ALPHA-NUMERIC INPUT DEVICE** (550): Connected to the bus via a bidirectional arrow.
 - CURSOR CONTROL DEVICE** (552): Connected to the bus via a bidirectional arrow.
 - DRIVE UNIT** (556): Contains a **MACHINE-READABLE MEDIUM** (560) with an **INSTRUCTIONS** block (562). It is connected to the bus via a bidirectional arrow.
 - SIGNAL GENERATION DEVICE** (558): Connected to the bus via a bidirectional arrow.

The central **BUS** (548) is represented by a vertical line with arrows at both ends, indicating bidirectional data flow between all connected components.

